



2826
#5/c
3/25/03 PAPER
Customer No. 22852
Attorney Docket No. 06720.0070

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Ming-Dou KER, et al.) Group Art Unit: 2826
Application No.: 10/062,714) Examiner: Mandala, V.
Filed: 02/05/2002)
For: SCR DEVICES IN SILICON-ON-)
INSULATOR CMOS PROCESS)
FOR ON-CHIP ESD)
PROTECTION)

Commissioner for Patents
Washington, DC 20231

Sir:

AMENDMENT

In reply to the Office Action dated December 20, 2002, Applicants submit the following amendment and remarks. This response is due by March 20, 2003, and is timely filed.

IN THE CLAIMS:

Please amend claims 32, 34, and 35 as follows:

A1
32. (Amended) The integrated circuit device as claimed in claim 31, wherein the cathode is coupled to at least one diode to prevent the rectifier from being triggered in a non-ESD operation.

34. (Amended) The method as claimed in claim 33, further comprising a step of biasing the p-type MOS transistor to trigger the rectifier.